

Electronic Acknowledgement Receipt

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|---|---|
| EFS ID: | 1832922 |
| Application Number: | 10757928 |
| International Application Number: | |
| Confirmation Number: | 6328 |
| Title of Invention: | Apparatus and methods for providing redundancy in integrated circuits |
| First Named Inventor/Applicant Name: | Joseph Huang |
| Customer Number: | 46627 |
| Filer: | Maximilian R. Peterson. |
| Filer Authorized By: | |
| Attorney Docket Number: | ALTR:023 |
| Receipt Date: | 01-JUN-2007 |
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| Application Type: | Utility |

Payment information:

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| Submitted with Payment | yes |
| Payment was successfully received in RAM | \$ 1240 |
| RAM confirmation Number | 2644 |
| Deposit Account | |

File Listing:

| Document Number | Document Description | File Name | File Size(Bytes) | Multi Part /.zip | Pages (if appl.) |
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|--|---|------------------|--------|----|----|
| 1 | Request for Continued Examination (RCE) | ALTR-023-RCE.pdf | 39850 | no | 1 |
| Warnings: | | | | | |
| This is not a USPTO supplied RCE SB30 form. | | | | | |
| Information: | | | | | |
| 2 | Amendment After Final | ALTR-023-ROA.pdf | 49815 | no | 10 |
| Warnings: | | | | | |
| Information: | | | | | |
| 3 | Extension of Time | ALTR-023-EOT.pdf | 24747 | no | 2 |
| Warnings: | | | | | |
| Information: | | | | | |
| 4 | Fee Worksheet (PTO-06) | fee-info.pdf | 8321 | no | 2 |
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| Total Files Size (in bytes): | | | 122733 | | |
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